



## Principal

DR.MRS.PRACHI WARNEKAR

### Educational Background: -

Institution	Year of Passing	Qualification	Details
<b>Indian Institute of Technology, Mumbai</b>	1982	<b>Ph.D. (Semi-conductor Devices)</b>	Ph.D. Thesis "Semiconductor-electrolyte Interface Study" (silicon and nickel oxide)
<b>Indian Institute of technology, Mumbai</b>	1974	<b>Master of Technology (Semi-conductor Devices) 60.7%</b>	M. Tech. Thesis: Construction and Testing of Argon Ion gas Laser.
<b>Vishveshvarya Regional College of Engineering Nagpur</b>	1971	<b>Bachelor of Engineering (Elec. Engg.) 66%</b>	First Class with Distinction
<b>Vidarbha Board of Secondary Education nagpur</b>	1965	<b>SSC 90%</b>	First class with distinction. Recipient of Gold Medals

### Membership of Professional Organizations:

- 1) IEEE (Membership No 41473663)
- 2) Indian Society for Technical Education (ISTE): Life Member

**Work Experience –Highlights:**

<b>Organization</b>	<b>Period</b>	<b>Position</b>
<b>Vidyalankar Institute of Technology Wadala East 400037</b>	26 August 08 till date	<b>Professor and Dean Development</b>
<b>Sardar Patel Institute of Technology, Andheri West, Mumbai 400058</b>	August 2006 to March 2008	<b>Principal, PROFESSOR</b> Head of the Electronics Engineering Dept.
<b>K.J. SOMAIYA COLLEGE OF ENGINEERING, VIDYAVIHAR Mumbai-400077</b>	Sept. 2005 to Feb. 2006	<b>Principal, PROFESSOR</b>
<b>D.J. SANGHAVI COLLEGE OF ENGINEERING MUMBAI- 400056.</b> (a College affiliated to University of Mumbai, India and rated “A” grade college by state government.)	April 2000 to July 2005  Jan. 98 to Mar. 2000  Dec 96 to Jan 98	<b>Vice-Principal, PROFESSOR and Head of the Electronics Engineering Dept.</b>  Professor and Head of the Electronics Engineering Dept.  Assistant professor,  Electronics Engineering dept.
<b>F.R. COLLEGE OF ENGINEERING, Bandra 400050</b> (a College Affiliated to University of Mumbai)	June 95 to Nov 96	<b>Visiting Faculty,</b> Electronics Engineering Dept.
<b>ALPHA X-RAY TECHNOLOGY</b>	June 93 to Dec 94	<b>R &amp; D Manager</b>
<b>ENTERPRENUER</b>	Aug 91 to Apr 93	<b>Sales and service consultant for Process Control Instruments</b>
<b>APTEK ELECTRONICS PVT. LTD. MUMBAI</b>	Jul 87 to Jul 91	<b>R &amp; D Manager</b>
<b>DYNALOG MICRO SYSTEMS, MUMBAI</b>	Sep 85 to Apr 87	<b>Senior R &amp; T Engineer</b>
<b>ELECTRONICS AND ELECTRICAL</b>	May 83 to Aug 85	<b>Senior R &amp; T Engineer</b>
<b>ENGINEERING COMPANY INDIAN INSTITUTE OF TECHNOLOGY, POWAI, Bombay</b>	Jul 82 to mar 83	<b>Senior Research Fellow</b>

**Work Experience:**

A) Vidyalkar Institute of Technology Laboratory infrastructure monitoring and development, Institute-Industry Interaction, ug/pg teaching, project evaluation, guidance

B) Sardar Patel Institute of Technology (Managed by Bharatiya Vidya Bhavan, Andheri West)